

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A method for data transmission between an IO (input-output) card with an add-on memory card and a host system, comprising the steps of:

providing the IO card with a socket therein;

inserting the memory card entirely into the socket of the IO card;

connecting the IO card to the host system; and

generating a bus control signal by the host through an IO controller on the IO card to selectively switch to a first data bus or a second data bus located in the IO card thereby to change data transmitting path between the memory card and the host system.

2. (Previously Presented) The method of claim 1, further comprising
directly connecting the first data bus and the second data bus to the host system, and
controlling a switch based on the bus control signal to selectively directly connect the inserted memory card to one of the first and second data bus.

3. (Cancelled)

4. (Previously Presented) The method of claim 1, wherein the memory card and the IO card further transmit data directly via the first bus without passing through a data bus of the host system.

5. (Original) The method of claim 1, wherein the memory card uses a portion of the data buses of the IO card to transmit data with the host system when the memory card is connected to the second data bus to transmit data, the IO card uses remaining data buses to transmit data with the host system.

6. (Original) The method of claim 1, wherein the IO card has a socket to electrically and mechanically couple with the memory card.

7. (Original) The method of claim 1, wherein the host system has an insertion slot to electrically and mechanically couple with the IO card.

8. (Currently Amended) A method for data transmission between an IO (input-output) card with an add-on memory card and a host system, the memory card being inserted in the IO card which is connected to the host system, the method comprising:

providing the IC card with a first data bus and a second data bus, the first data bus and the second data bus being directly connected to the host system;

providing a switch to selectively directly connect the inserted memory card to one of the first and second data bus; and

generating a bus control signal by the host through an IO controller located in the IO card, thereby controlling the switch to selectively directly connect the inserted memory card to one of the first and second data bus.

9. (Cancelled)

10. (Previously Presented) The method of claim 8, wherein the memory card and the IO card further transmit data directly via the first bus without passing through a data bus of the host system.

11. (Cancelled)

12. (Original) The method of claim 8, wherein the memory card uses a portion of the data buses of the IO card to transmit data with the host system when the memory card is connected to the second data bus to transmit data, the IO card uses remaining data buses to transmit data with the host system.

13. (Original) The method of claim 8, wherein the IO card has a socket to electrically and mechanically couple with the memory card.

14. (Original) The method of claim 8, wherein the host system has an insertion slot to electrically and mechanically couple with the IO card.

15. (New) The method of claim 1, wherein when the second data bus is switched, the memory card employs a portion of the data buses of the IO card to transmit data to the host system.

16. (New) The method of claim 1, wherein when the first data bus is switched, the IO card and the memory card share all data buses of the IO card to interactively transmit data to the host.

17. (New) The method of claim 8, wherein when the second data bus is switched, the memory card employs a portion of the data buses of the IO card to transmit data to the host system.

18. (New) The method of claim 8, wherein when the first data bus is switched, the IO card and the memory card share all data buses of the IO card to interactively transmit data to the host.